BT137 Series

8.0A 4Quadrants TRIACs

Product Summary

Symbol	Value	Unit
$I_{T(RMS)}$	8.0	A
V_{DRM} V_{RRM}	600 / 800	V
V_{TM}	1.55	V

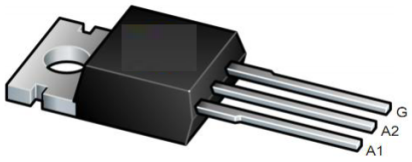
Feature

With high ability to withstand the shock loading of large current,With high commutation performances, 4 quadrants products especially recommended for use on inductive load.

Application

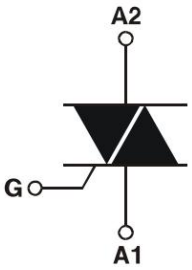
Washing machine, vacuums, massager, solid state relay, AC Motor speed regulation and so on.

Package

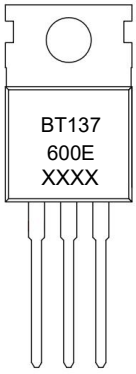


TO-220C

Circuit diagram



Marking



Absolute maximum ratings (Ta=25℃ unless otherwise noted)

Parameter	Symbol	Value		Unit
Repetitive peak off-state voltage	V_{DRM}	600 / 800		V
Repetitive peak reverse voltage	V_{RRM}	600 / 800		V
RMS on-state current	$I_{T(RMS)}$	8		A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	65		A
I^2t value for fusing (tp=10ms)	I^2t	21		A^2s
Critical rate of rise of on-state current (I_G =2×I_{GT})	dI_T/dt	Ⅰ-Ⅱ-Ⅲ	50	A/μs
		Ⅳ	10	
Peak gate current	I_{GM}	2		A
Average gate power dissipation	$P_{G(AV)}$	0.5		W
Junction Temperature	T_J	-40 ~ +125		℃
Storage Temperature	T_{STG}	-40 ~ +150		℃

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition		Value		Unit
Gate trigger current	I_{GT}	V_D =12V I_T = 0.1A T_j =25℃	Ⅰ-Ⅱ-Ⅲ	MAX.	10	mA
			Ⅳ		25	
Gate trigger voltage	V_{GT}		Ⅰ-Ⅱ-Ⅲ-Ⅳ	MAX.	1.3	V
Gate non-trigger voltage	V_{GD}	V_D =V_{DRM} T_j =125℃		MIN.	0.2	V
latching current	I_L	V_D =12V I_{GT} = 0.1A T_j =25℃	Ⅰ-Ⅲ-Ⅳ	MAX.	25	mA
			Ⅱ		35	
Holding current	I_H		Ⅰ-Ⅱ-Ⅲ-Ⅳ	MAX.	20	mA
Critical-rate of rise of commutation voltage	dV_D/dt	V_D=2/3V_{DRM} Gate Open T_j =125℃		MIN.	20	V/μs
STATIC CHARACTERISTICS						
Forward "on" voltage	V_{TM}	I_{TM} =10A tp=380μs		MAX.	1.55	V
Repetitive Peak Off-State Current	I_{DRM}	V_D =V_{DRM} V_R =V_{RRM}	T_j=25℃	MAX.	5	μA
Repetitive Peak Reverse Current	I_{RRM}		T_j=125℃	MAX.	1	mA
THERMAL RESISTANCES						
Thermal resistance	Rth(j-c)	Junction to case(AC)		TYP.	1.6	℃/W
	Rth(j-a)	Junction to ambient		TYP.	60	℃/W

Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

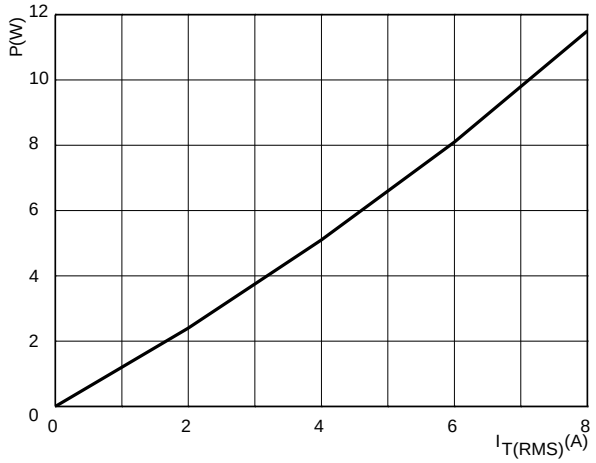


FIG.2: RMS on-state current versus case temperature (full cycle)

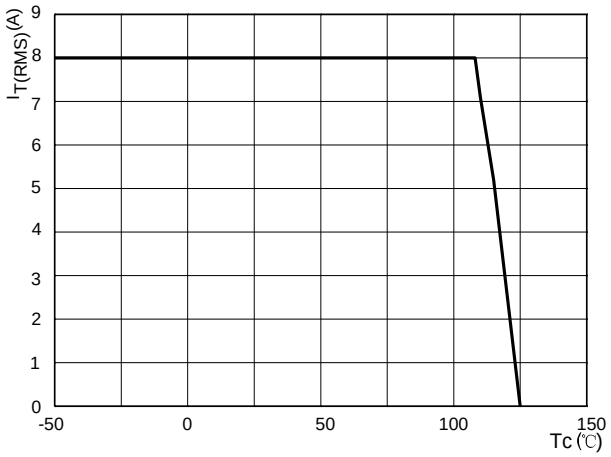


FIG.3: Surge peak on-state current versus number of cycles

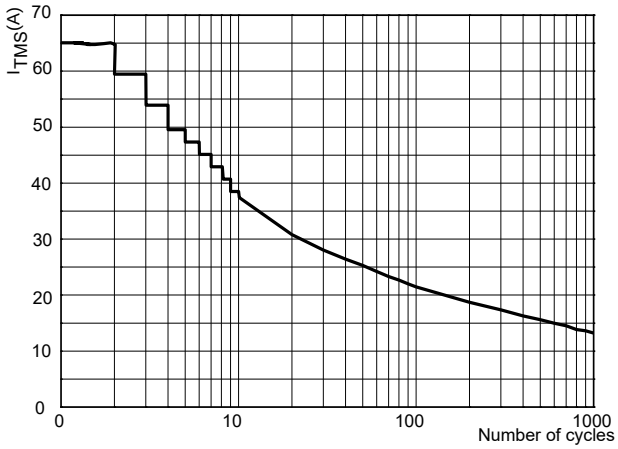


FIG.4: On-state characteristics (maximum values)

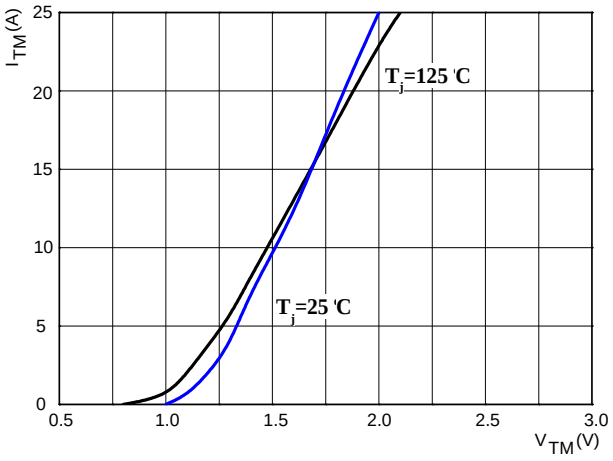


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10ms

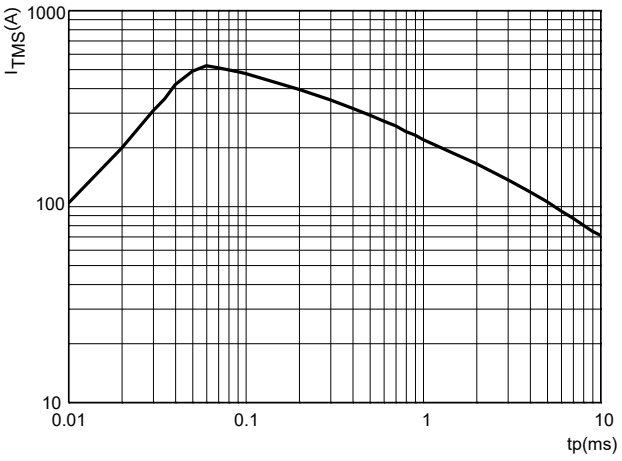
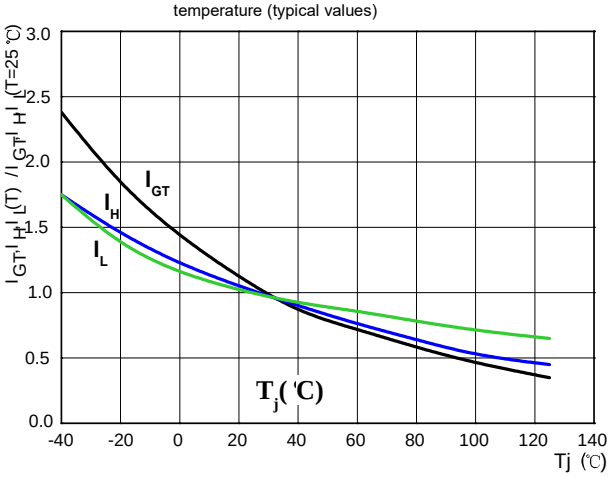
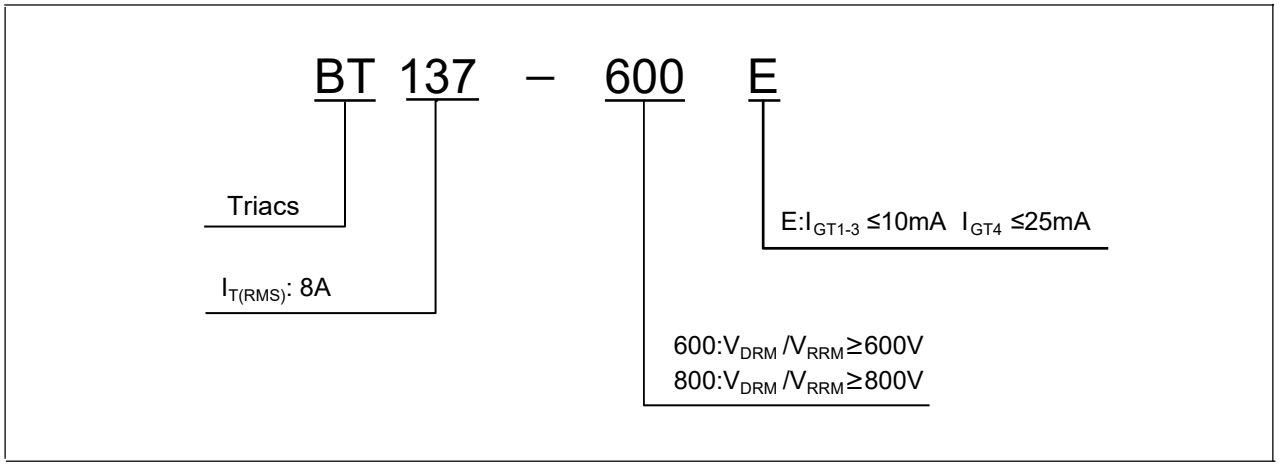


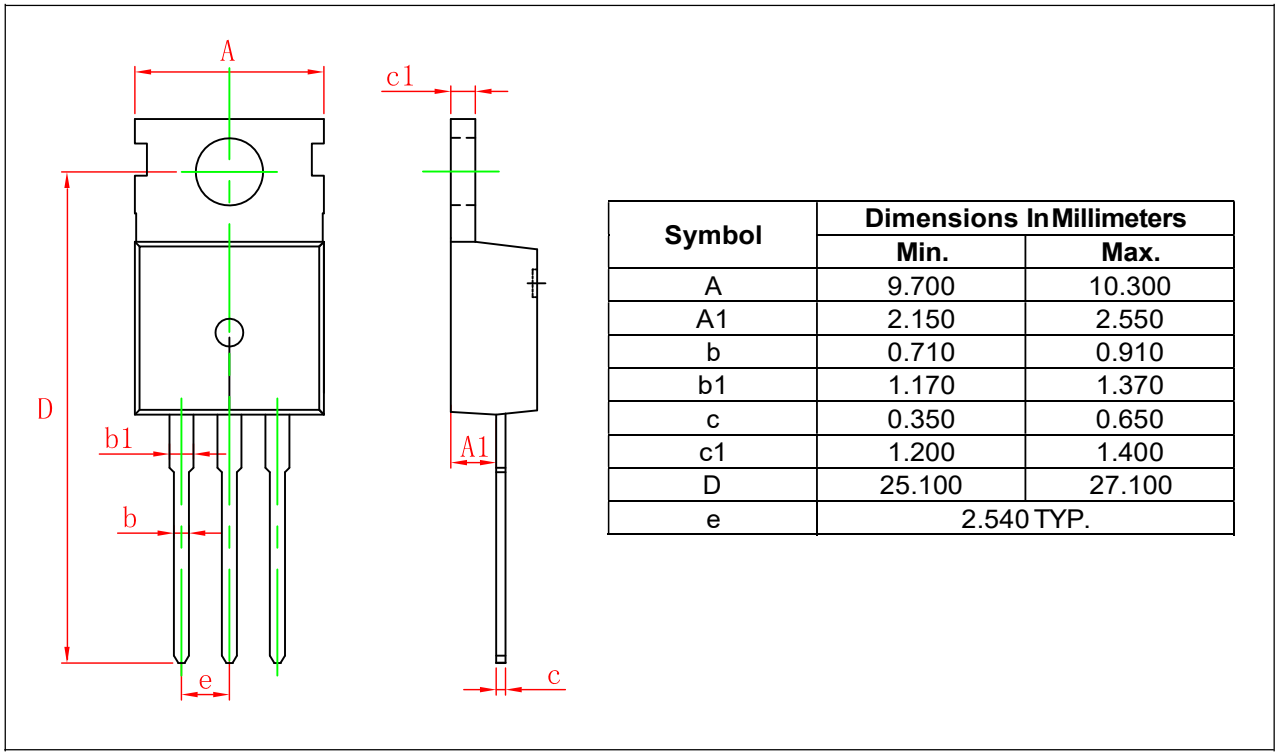
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



Ordering Information



TO-220C Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	9.700	10.300
A1	2.150	2.550
b	0.710	0.910
b1	1.170	1.370
c	0.350	0.650
c1	1.200	1.400
D	25.100	27.100
e	2.540 TYP.	